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Application / Conf. No.	10/716,947	6459
Filing Date	November 18, 2003	
First Named Inventor	Jay T. Young	
Examiner Name	Nghia M. Doan	
Art Unit	2825	
Patent No.		
Attorney Docket Number	X-1386 US	

ENCLOSURES (check all that apply)

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<input type="checkbox"/> Preliminary Amendment	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
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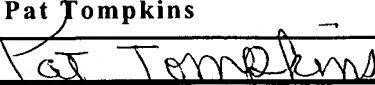
Remarks

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X-1386 US
10/716,947.

PATENT
Conf. No.: 6459

IN THE UNITED STATES PATENT OFFICE

Applicants: Jay T. Young et al.
Assignee: Xilinx, Inc.
Title: "Methods of Routing Programmable Logic Devices to Minimize Programming Time"
Serial No.: 10/716,947 File Date: 11-18-03
Examiner: Nghia M. Doan Art Unit: 2825
Docket No.: X-1386 US Conf. No.: 6459

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AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Dear Sir:

In response to the Office Action dated March 10, 2006, reconsideration and allowance of the application are respectfully requested in view of the amendment and remarks below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 11 of this paper.